

Electrical Design Engineer

Job Description:

Participate as a team member in the technical development of schematic, design development, construction documents, specifications, and construction administration for institutional and commercial building projects.

Job Duties:

- Electrical engineering of power and lighting systems for institutional, retail and commercial building projects. Prepare calculations and well-coordinated, technically sound construction documents that are cost effective, clear, efficient, and constructible in a friendly work environment.
- Communicate and coordinate with architects, owners, other engineering disciplines, vendors, building officials, other consultants, etc. throughout the design phases of the building project.
- Perform engineering calculations including energy, load, photometrics, voltage drop, etc.
- Must meet client schedules and internal design and construction administration time budgets.
- Office construction administration including review of shop drawings, respond to RFI's, and coordinate with contractors, design team, building officials, owner, etc. through the construction phase of the building project.
- Able to travel to site visits

Job Requirements:

- Bachelor of Science degree in Electrical Engineering or minimum of 5 years of related experience.
- Must possess excellent oral and written communication skills.
- Efficient in Microsoft Word and Excel.
- Familiar with AutoCAD and Revit.
- FE/EIT a plus.
- Capable of attending and engaging in meetings with clients, owners, contractors, etc. in a positive and productive manner.
- Possess a good work ethic and be efficient in job duties.
- Willing to work extra hours above 40 hours a week when necessary to meet project deadlines and client demands.

Job Type: Full-time

Benefits:

• 401(k)

- 401(k) matchingDisability insurance
- Flexible schedule
- Health insurance
- Paid time off
- Professional development assistance

To Apply:

Interested candidates please send resume and cover letter to Justin Quinlan, CAD Manager at JQuinlan@epic-eeg.com